

FIG. 1
 PRIOR ART

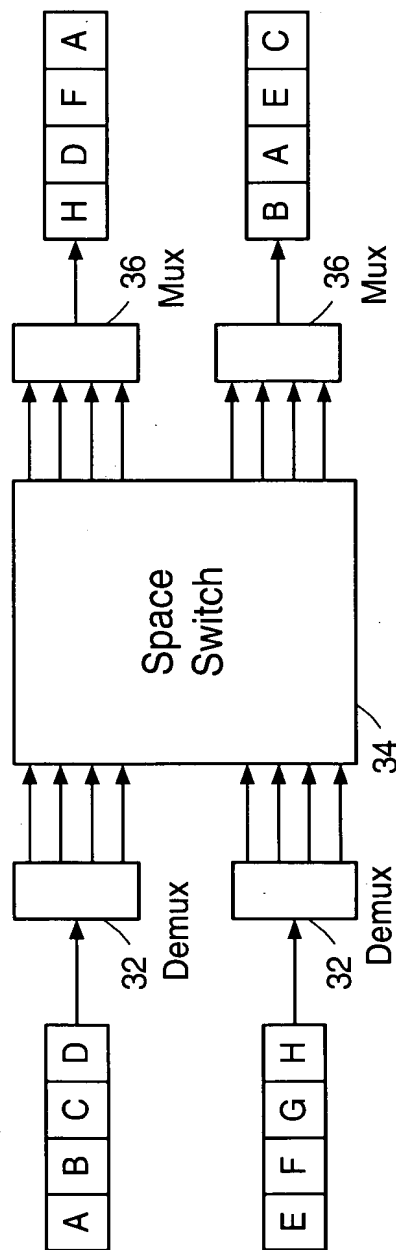


FIG. 2
 PRIOR ART

FIG. 3

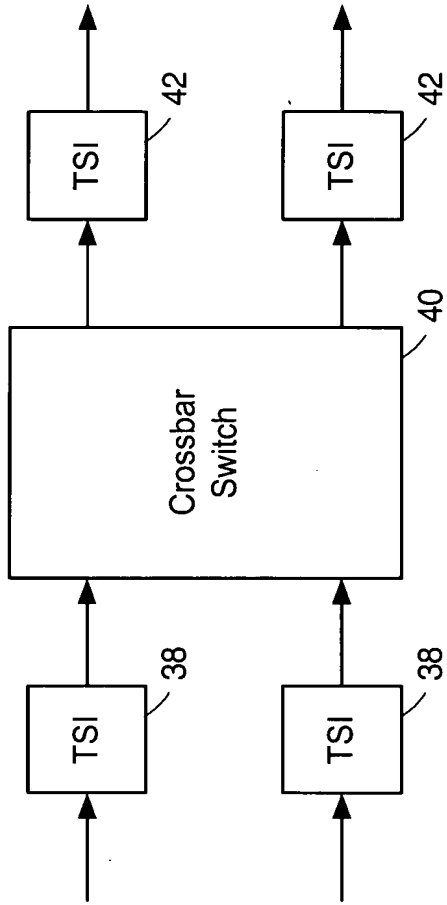


FIG. 3
PRIOR ART

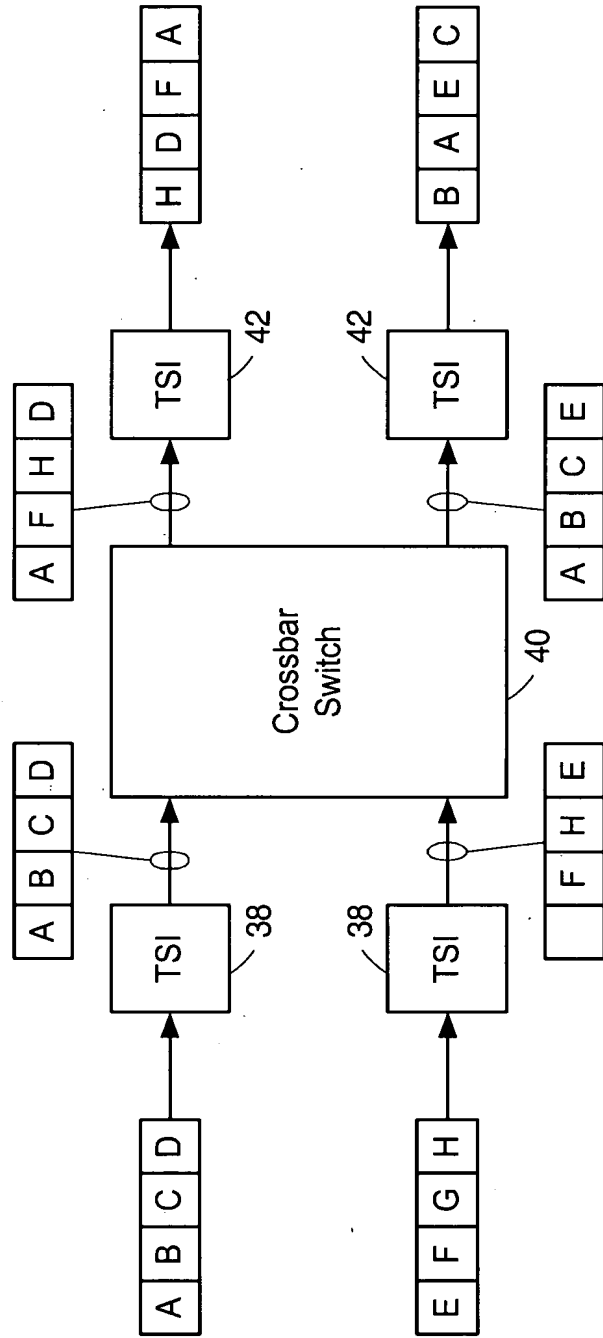


FIG. 4
PRIOR ART

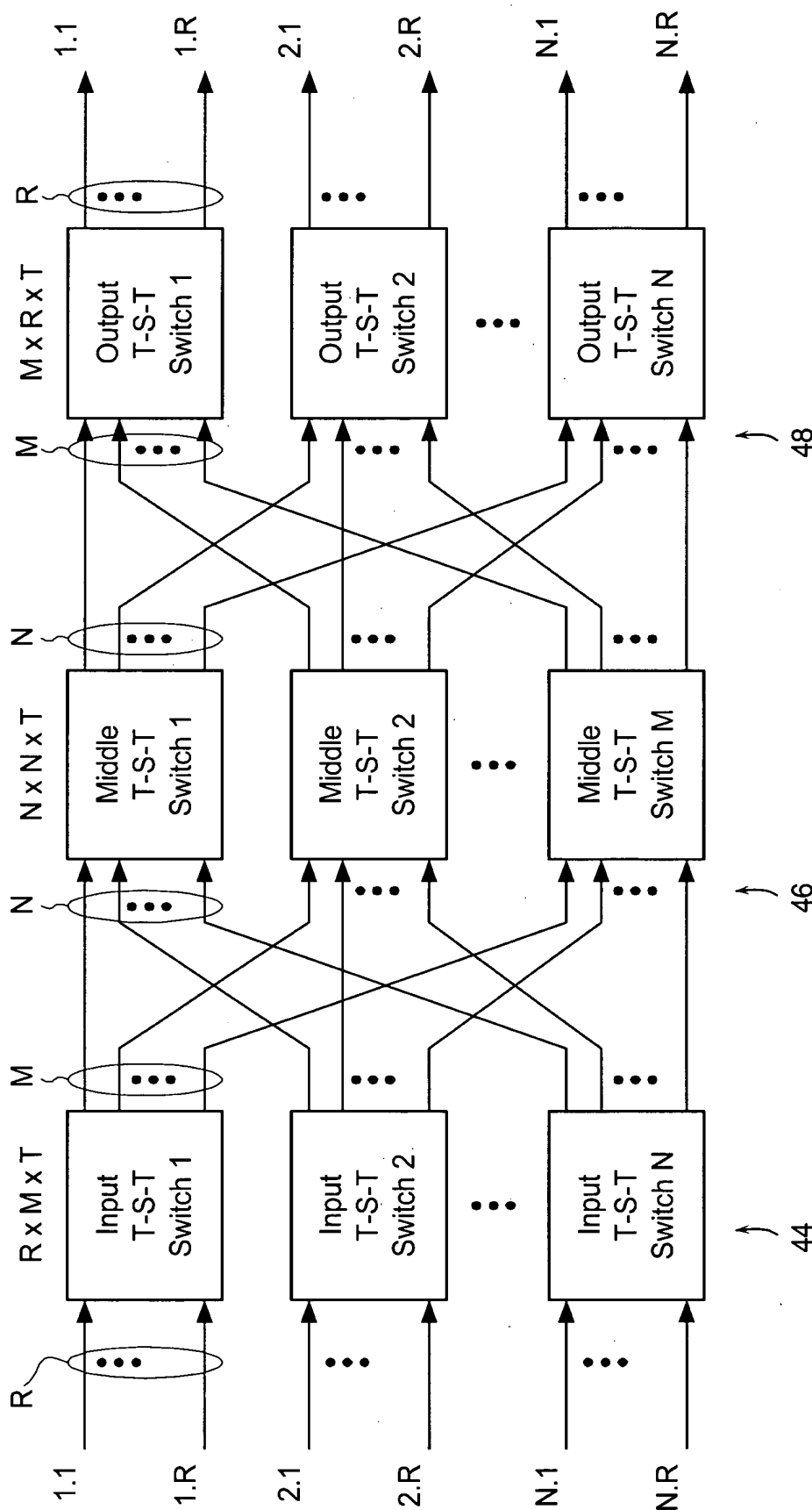


FIG. 5

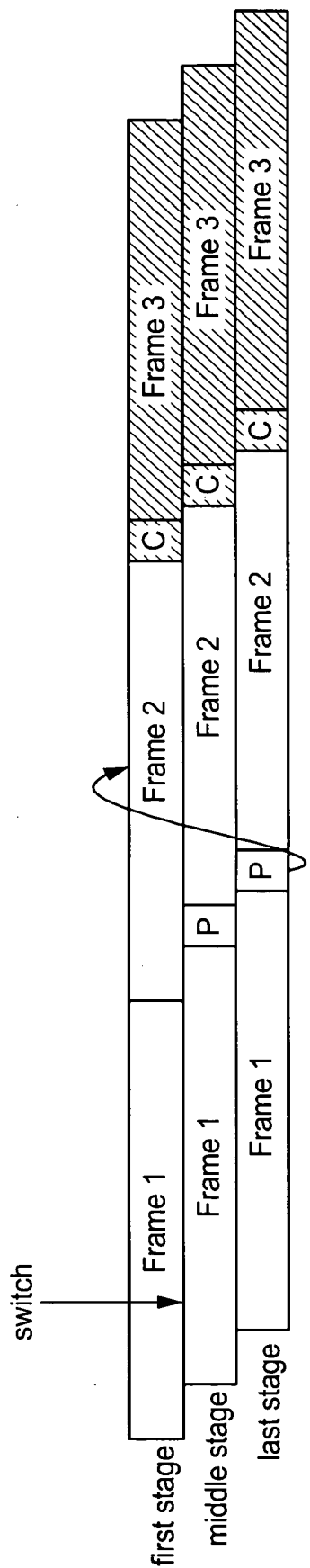


FIG. 6

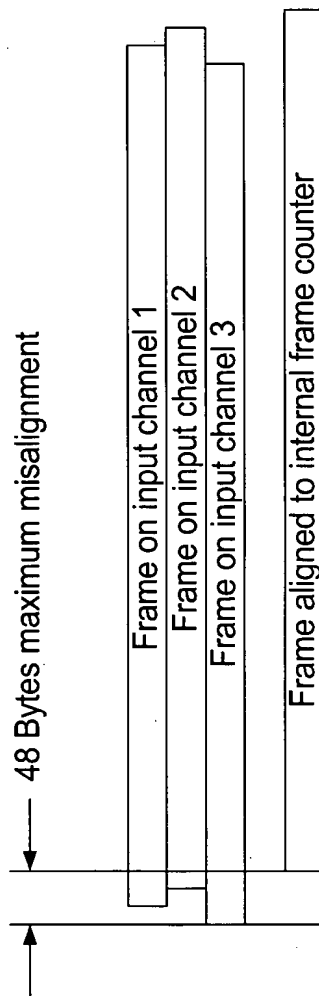


FIG. 13

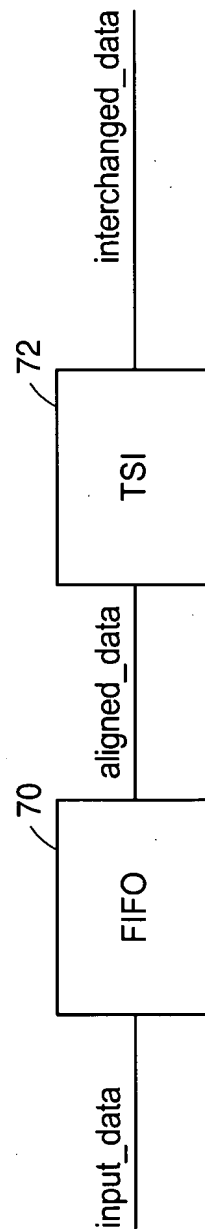
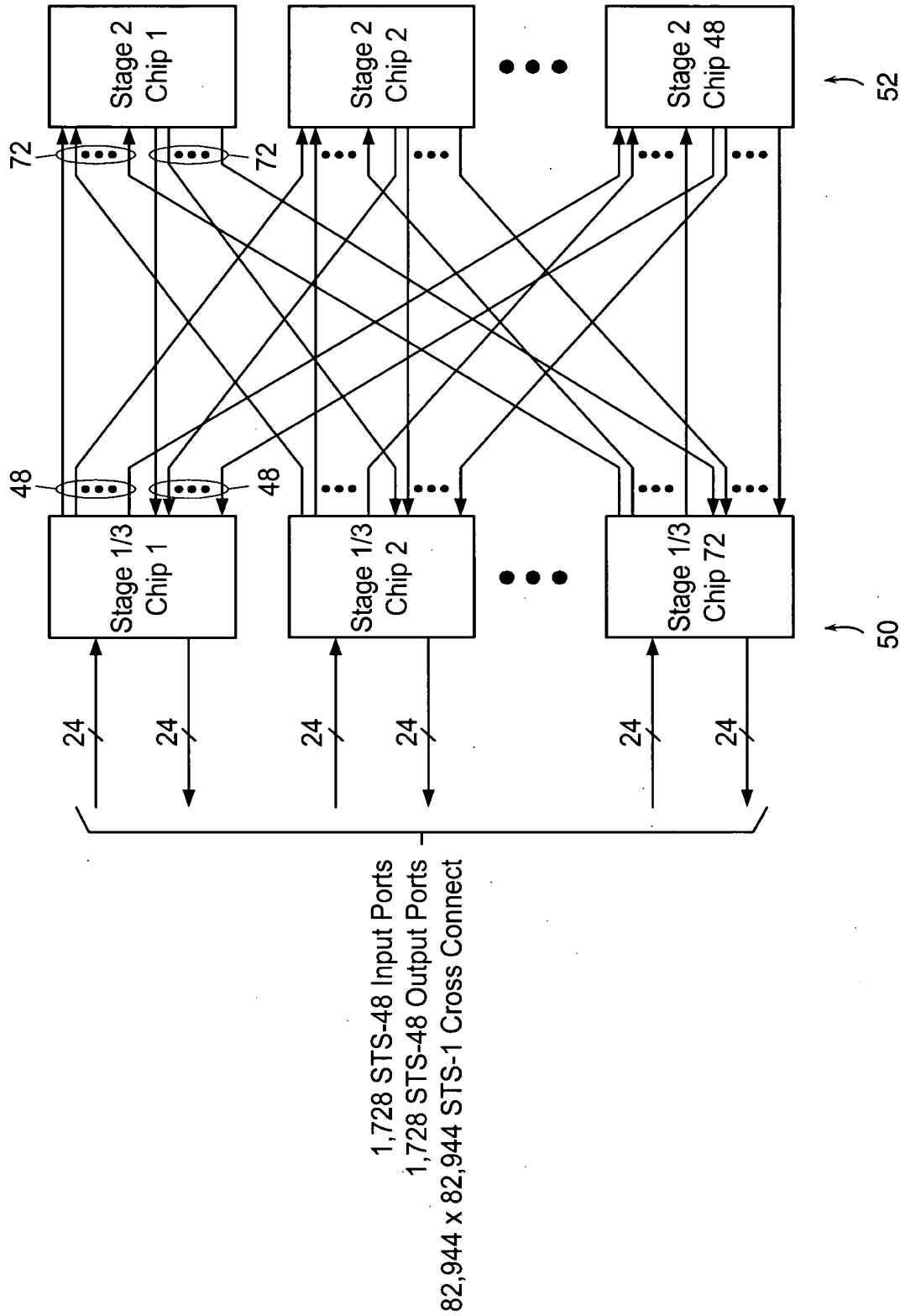


FIG. 14
PRIOR ART



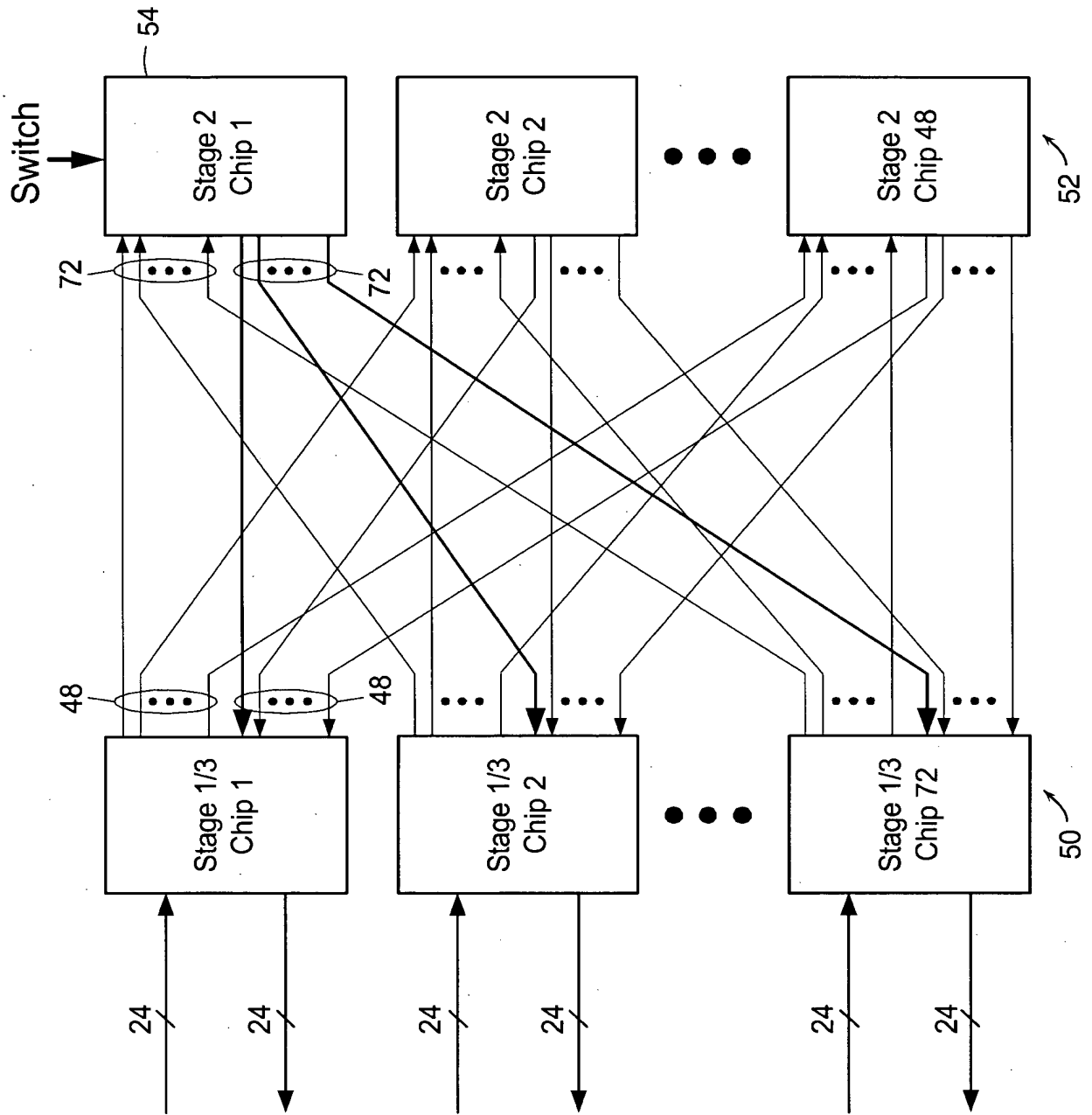


FIG. 8

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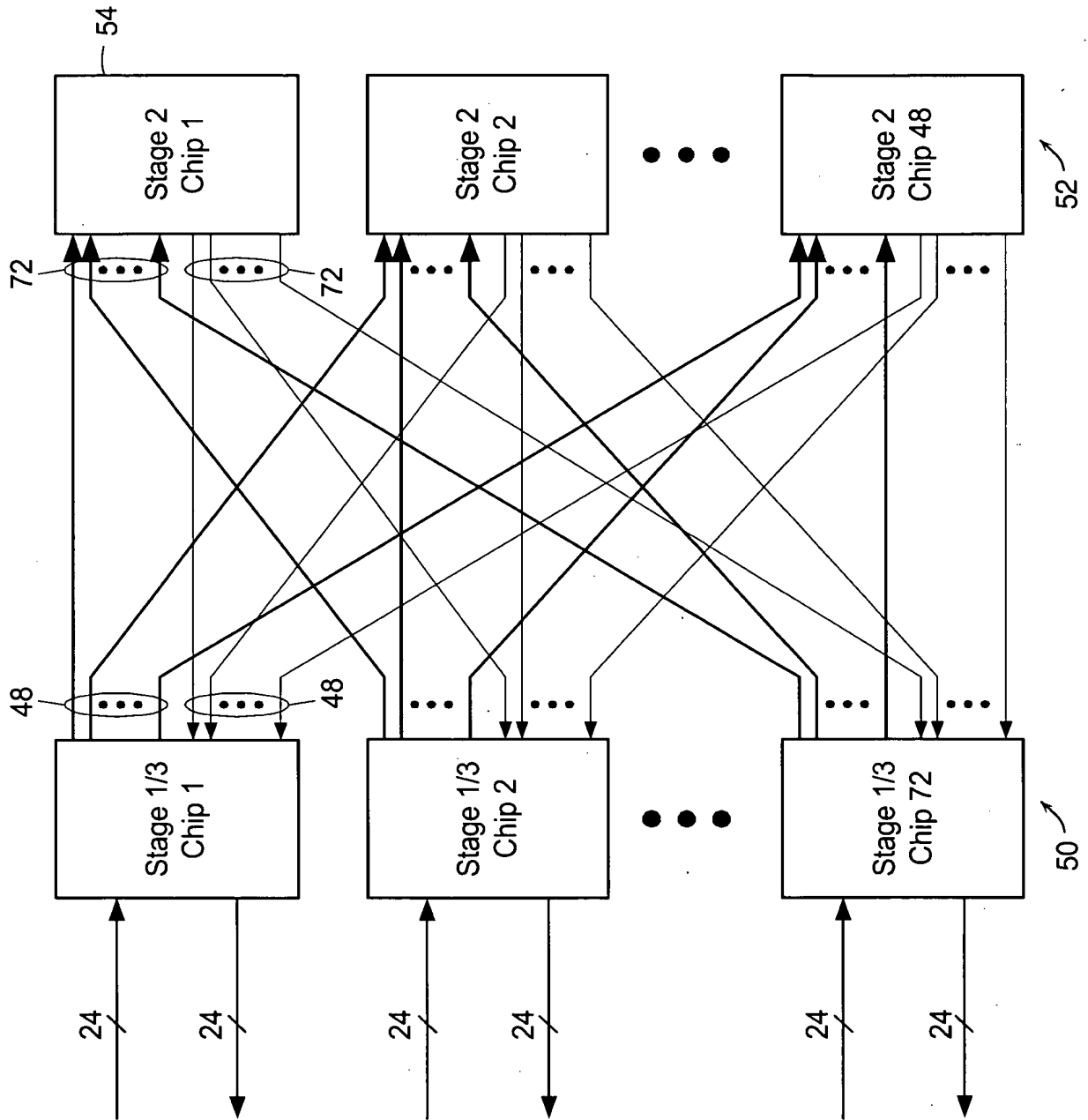


FIG. 9

FIG. 9

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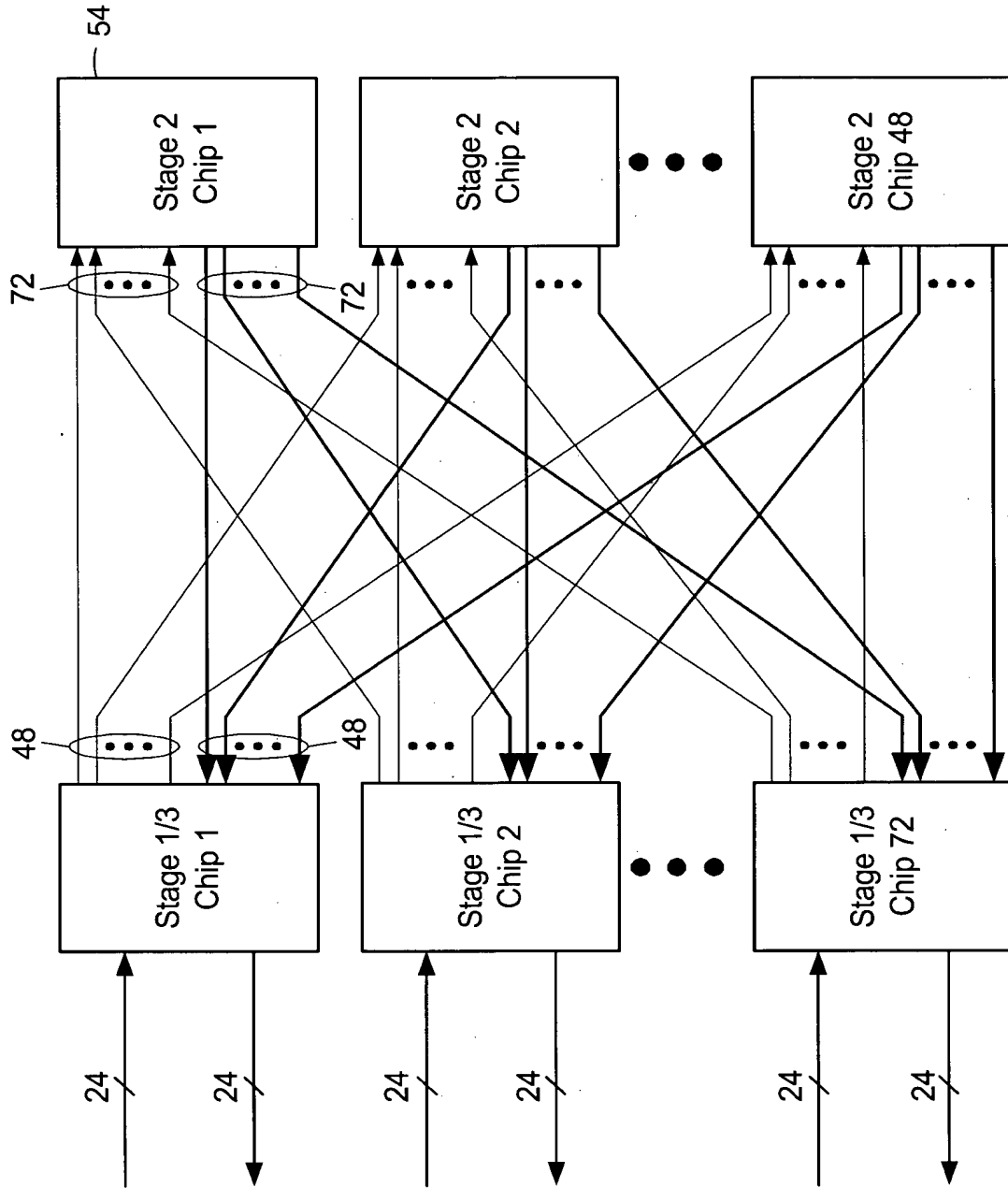
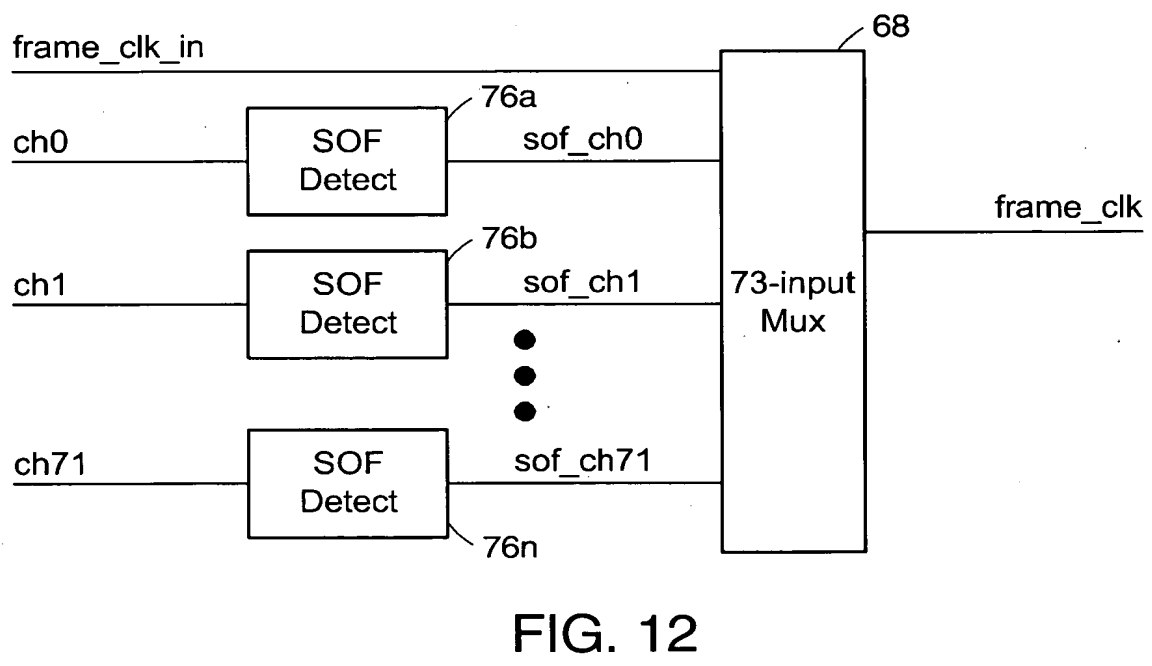
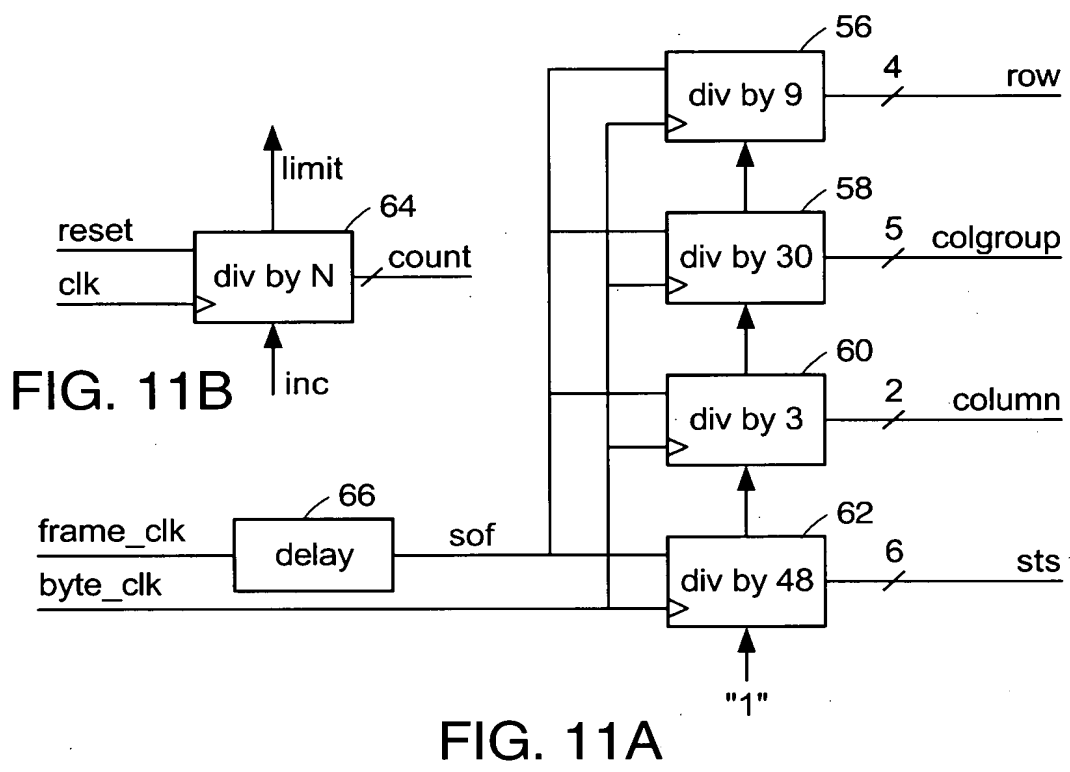


FIG. 10

FIG. 10



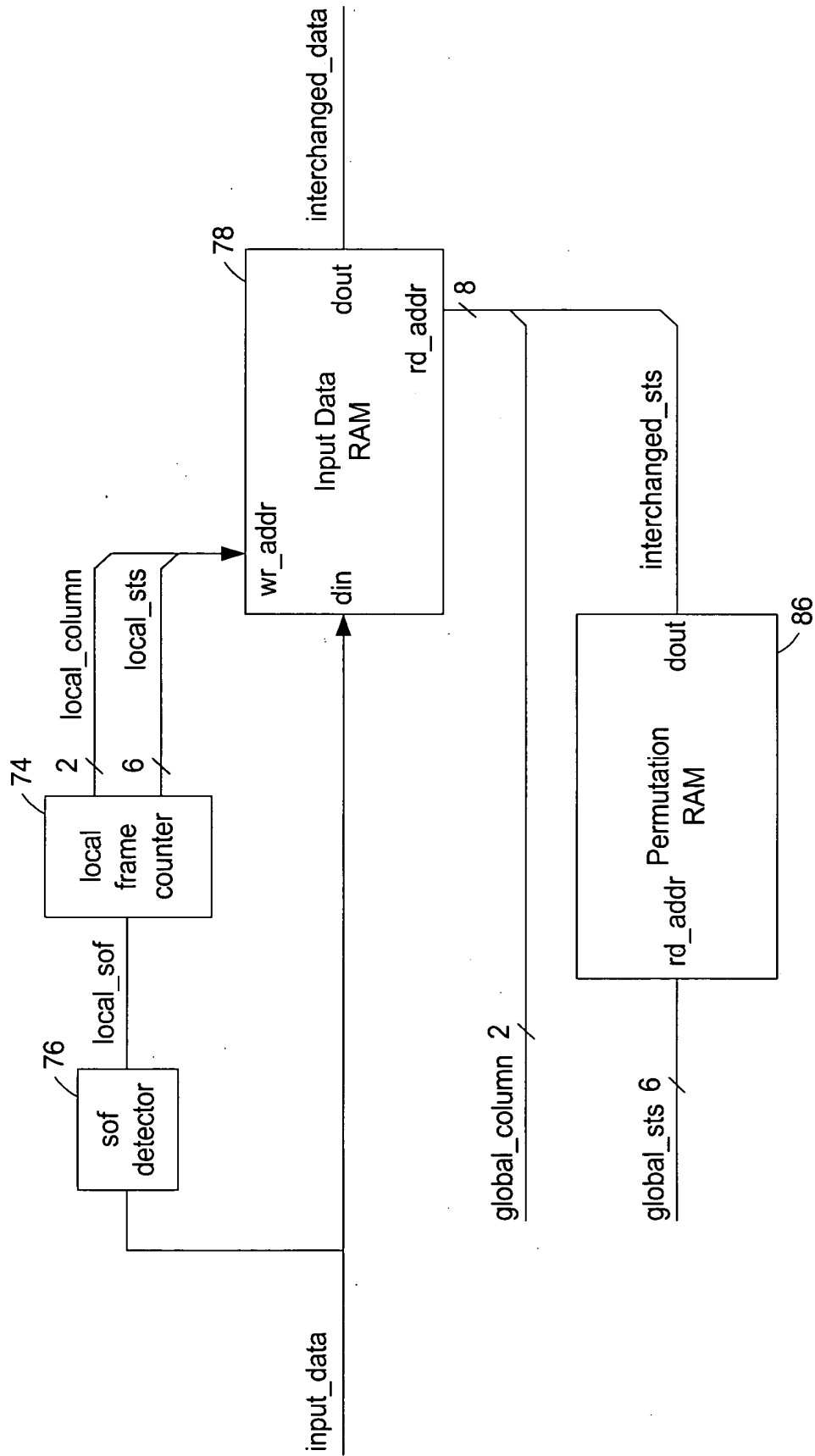


FIG. 15

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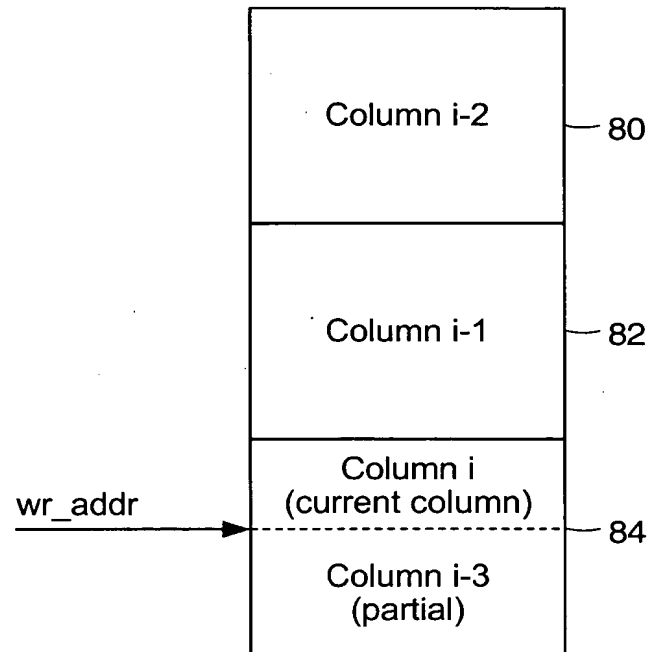


FIG. 16

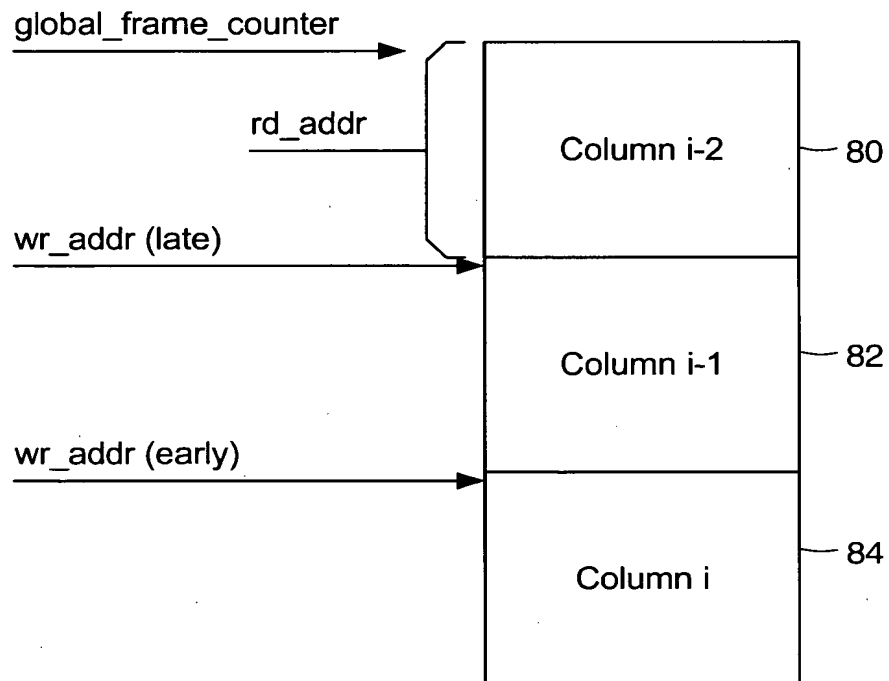


FIG. 17

09761538-040901

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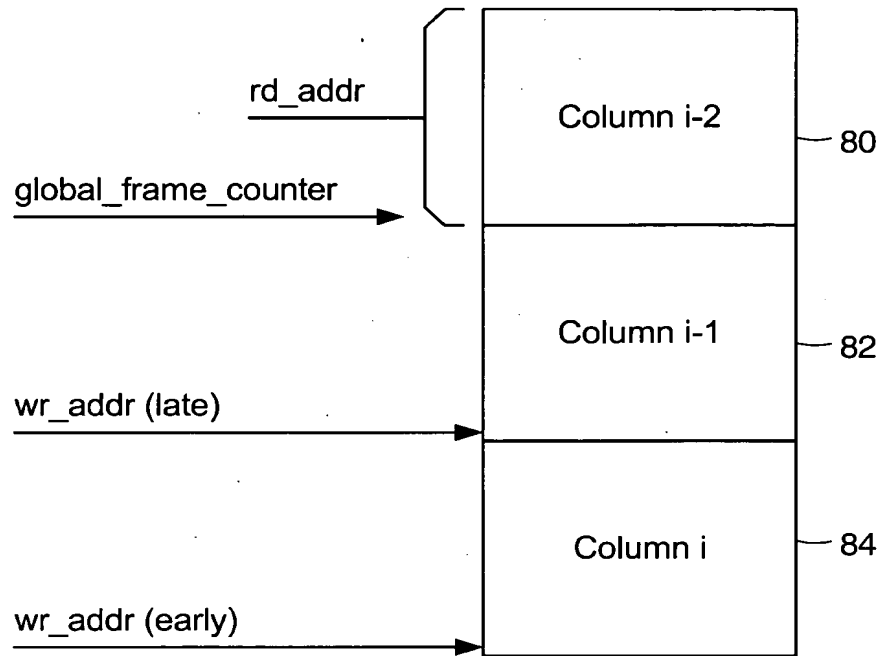


FIG. 18

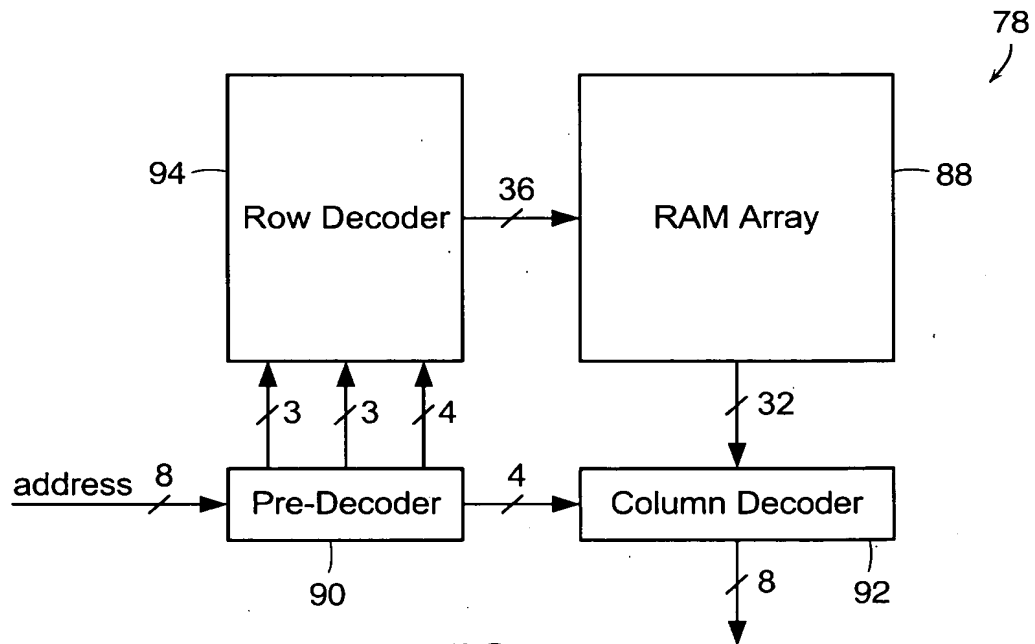


FIG. 19

09761538.040901

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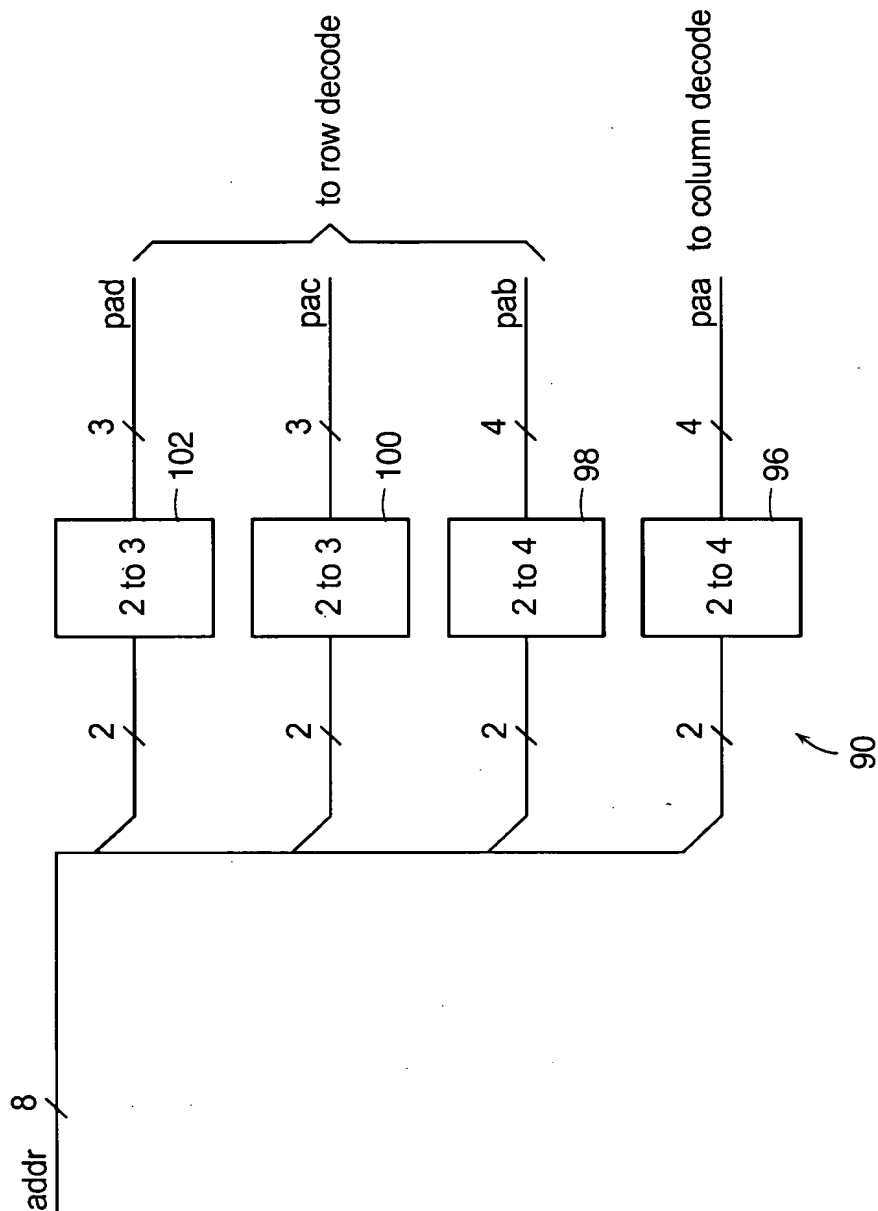


FIG. 20

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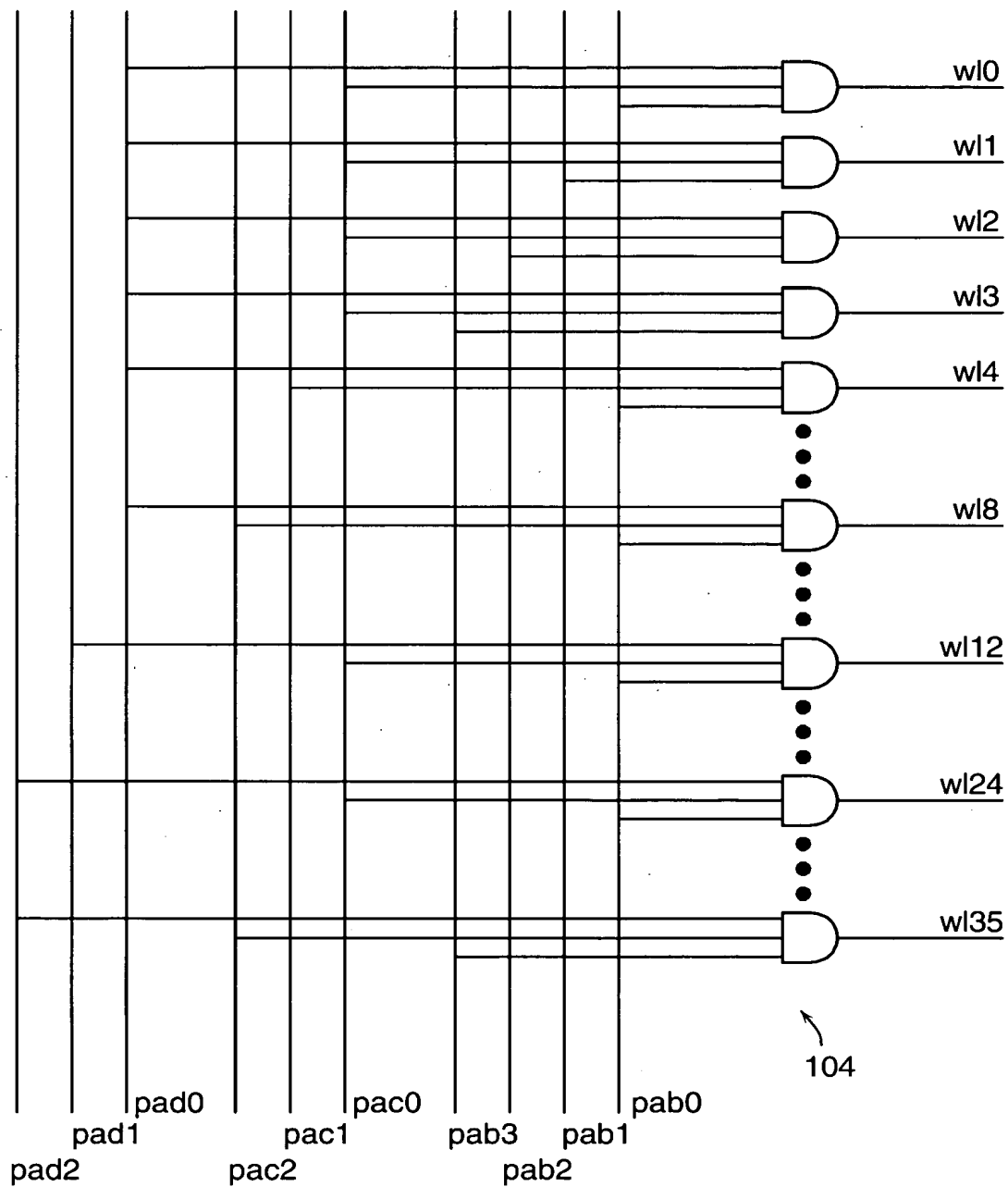


FIG. 21

15/16

Address
(hex)

FF

Not Used

B0

AF

Buffer 2

Row
(decimal)

35

Address
(hex)

AF

80

7F

Not Used

70

6F

Buffer 1

24

23

Buffer 2

80

6F

40

3F

Not Used

30

2F

Buffer 0

12

11

Buffer 1

40

2F

00

00

Buffer 0

00

FIG. 22

09/761,538 .040901

FO6040" 8EST9260

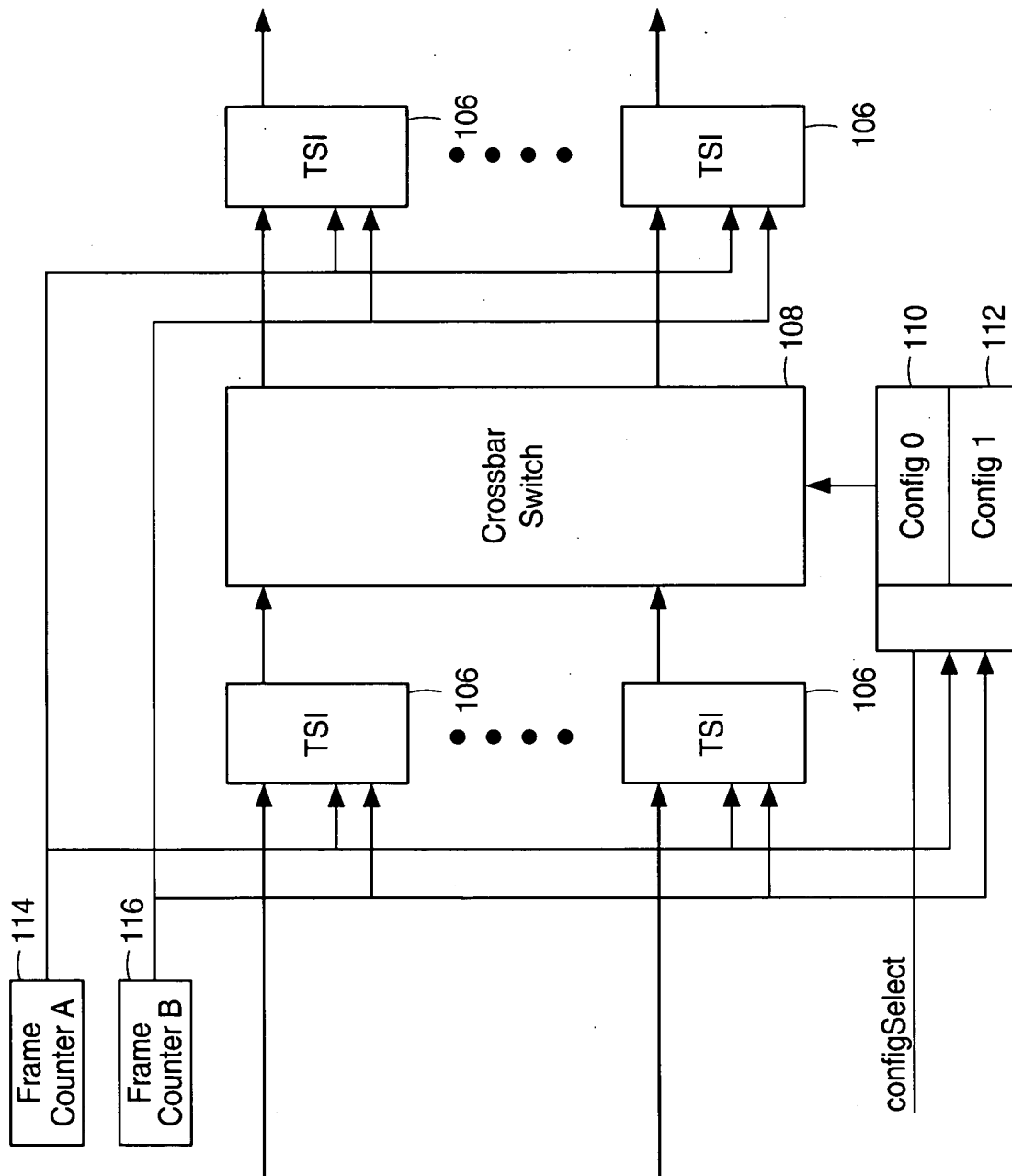


FIG. 23

FIG. 23